| Electronic Patent | App | lication Fee | Transm | ittal | | | |
|---|-----|--|----------|--------|-------------------------|--|--|
| Application Number: | 10 | 10810035 | | | | | |
| Filing Date: | 26- | 26-Mar-2004 | | | | | |
| Title of Invention: | | Non-volatile transistor memory array incorporating read-only elements with single mask set | | | | | |
| First Named Inventor/Applicant Name: | Alt | Albert S. Weiner | | | | | |
| Filer: | Da | David W. Black/Nicole Jack | | | | | |
| Attorney Docket Number: | 280 | 2800.450US1 | | | | | |
| Filed as Large Entity | | | | | | | |
| Utility under 35 USC 111(a) Filing Fees | | | | | | | |
| Description | | Fee Code | Quantity | Amount | Sub-Total in USD(\$) | | |
| Basic Filing: | | | | | , | | |
| Pages: | | | | | | | |
| Claims: | | | | | | | |
| Miscellaneous-Filing: | | | | | | | |
| Petition: | | | | | | | |
| Patent-Appeals-and-Interference: | | | | | | | |
| | | | | | | | |
| Post-Allowance-and-Post-Issuance: | | | | | | | |
| Post-Allowance-and-Post-Issuance: Extension-of-Time: | | | | | | | |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|----------------|-------------------|----------|--------|-------------------------|
| Miscellaneous: | | | | |
| | Total in USD (\$) | | | 120 |